

Figure 1

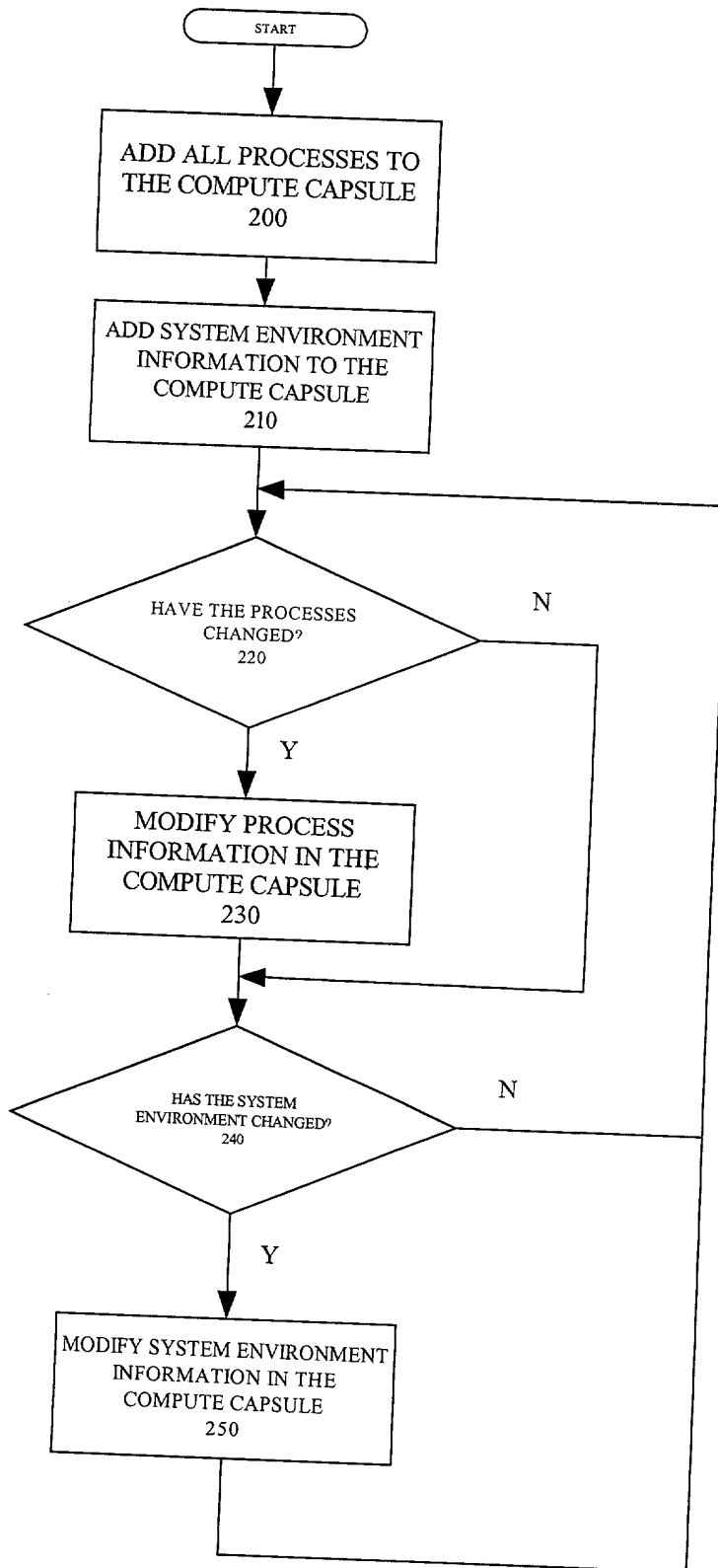


FIGURE 2

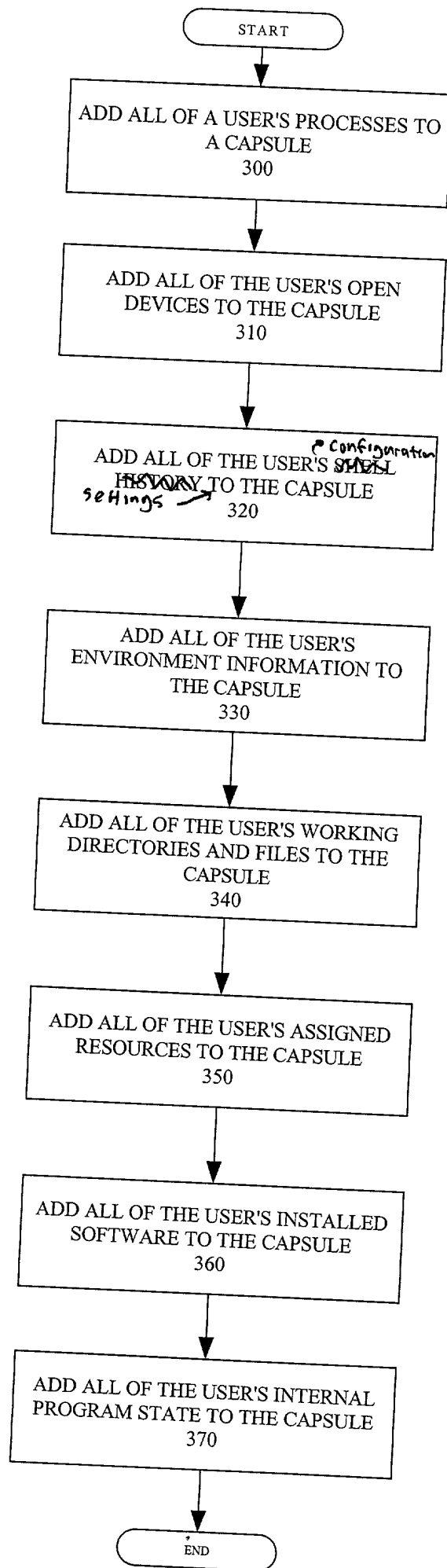


FIGURE 3A

FIGURE 3B

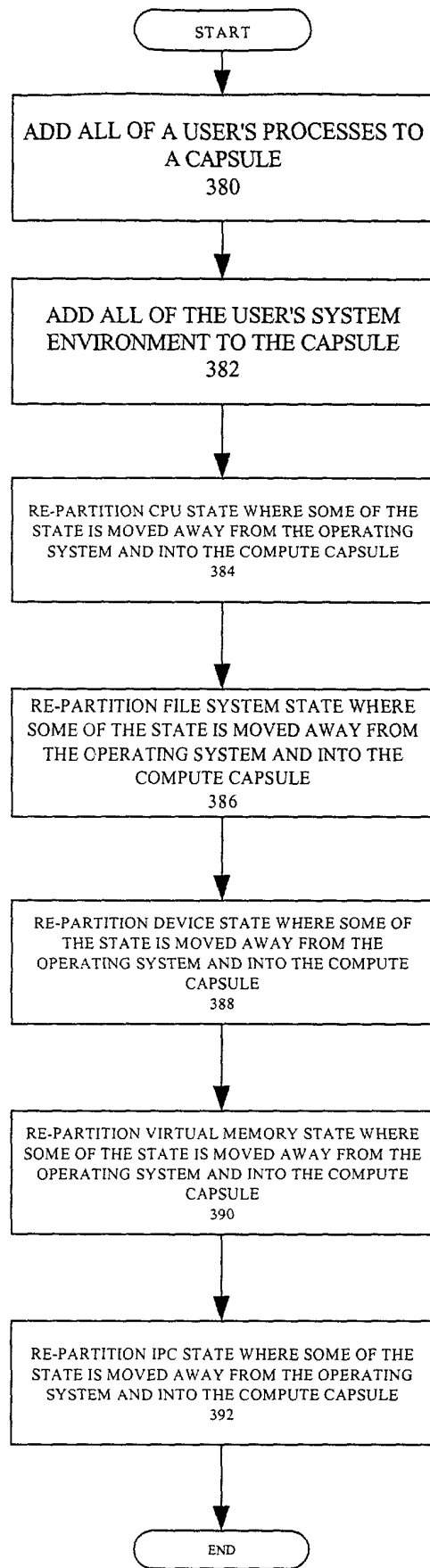
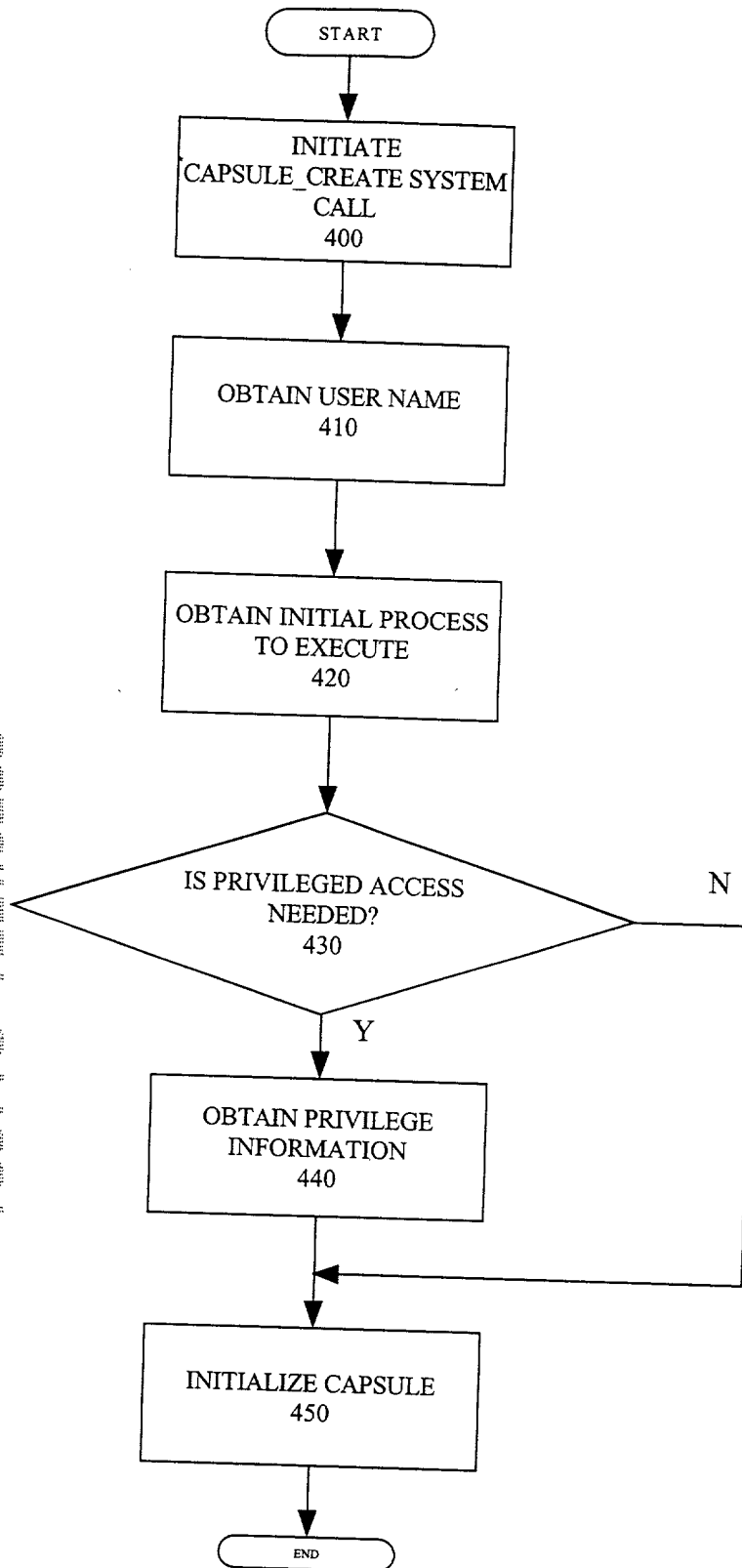


FIGURE 4



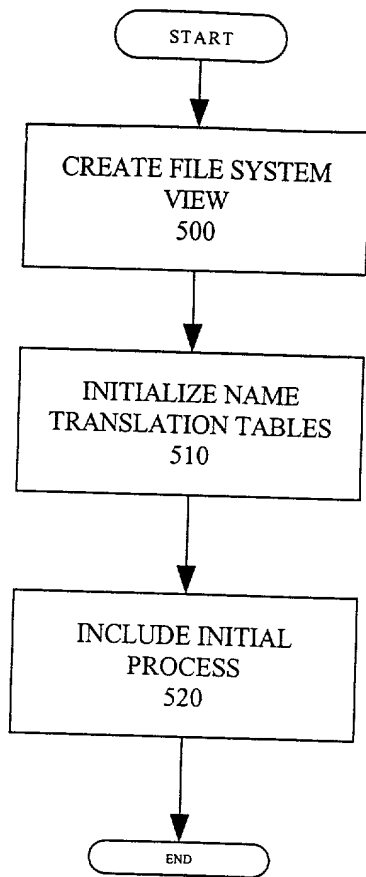


FIGURE 5

FIGURE 6

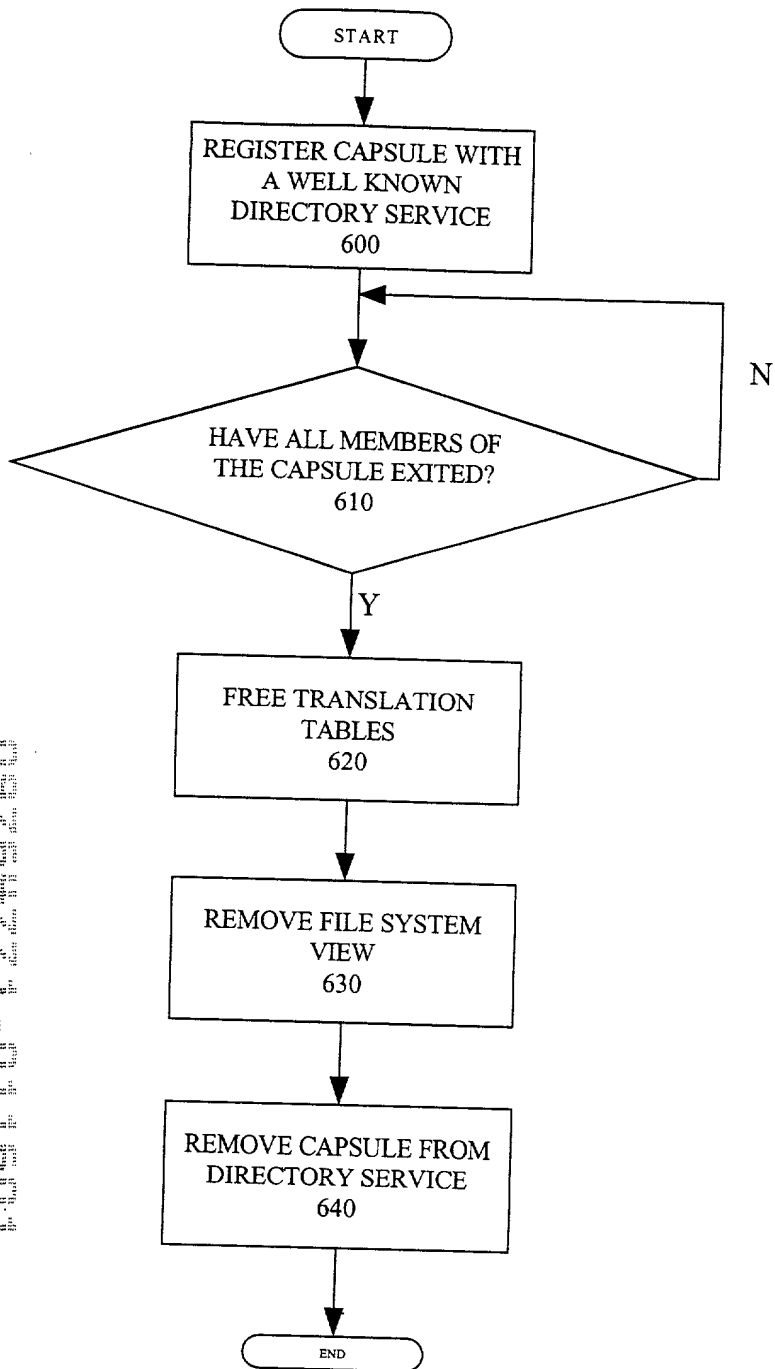


FIGURE 7

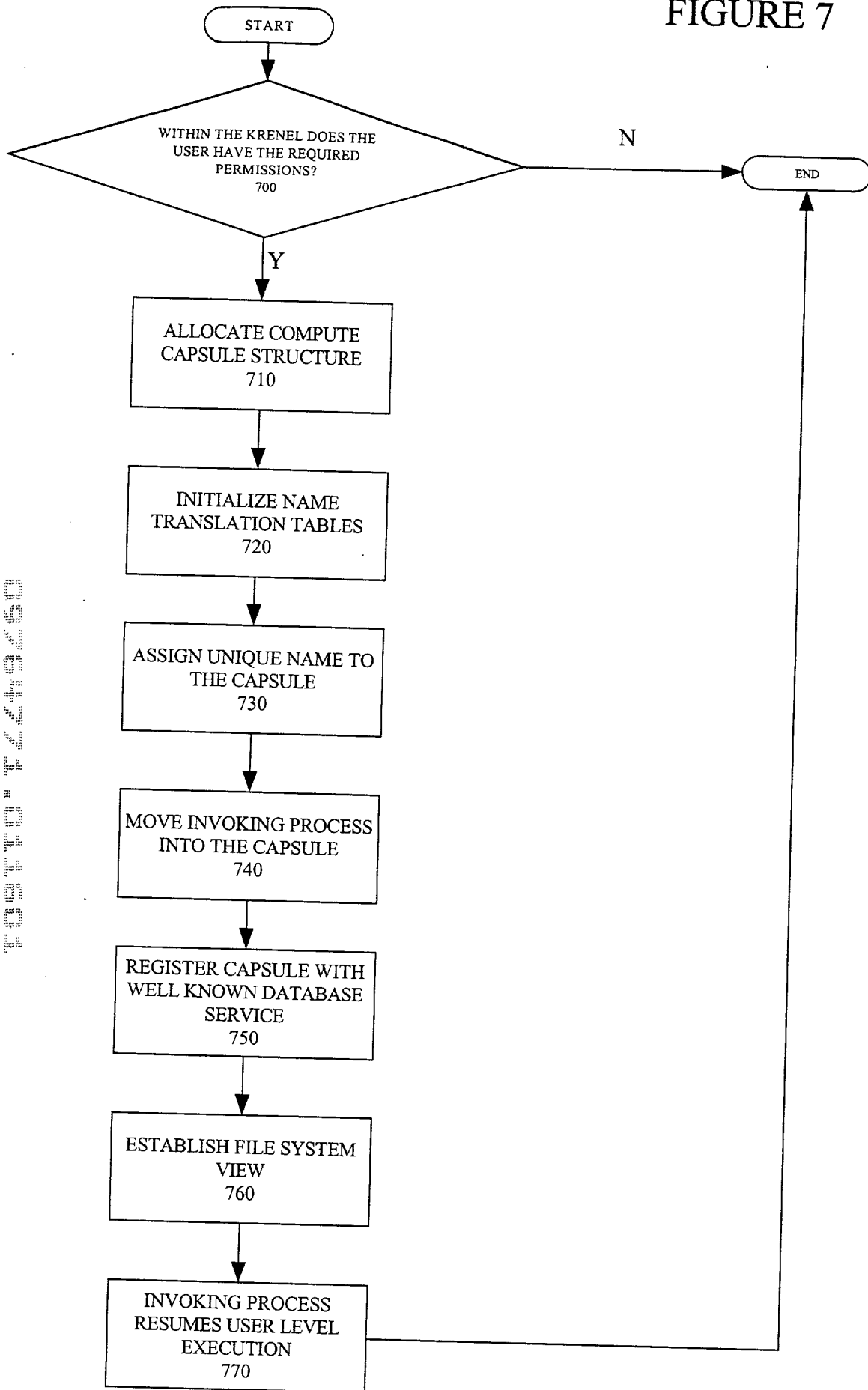


FIGURE 8

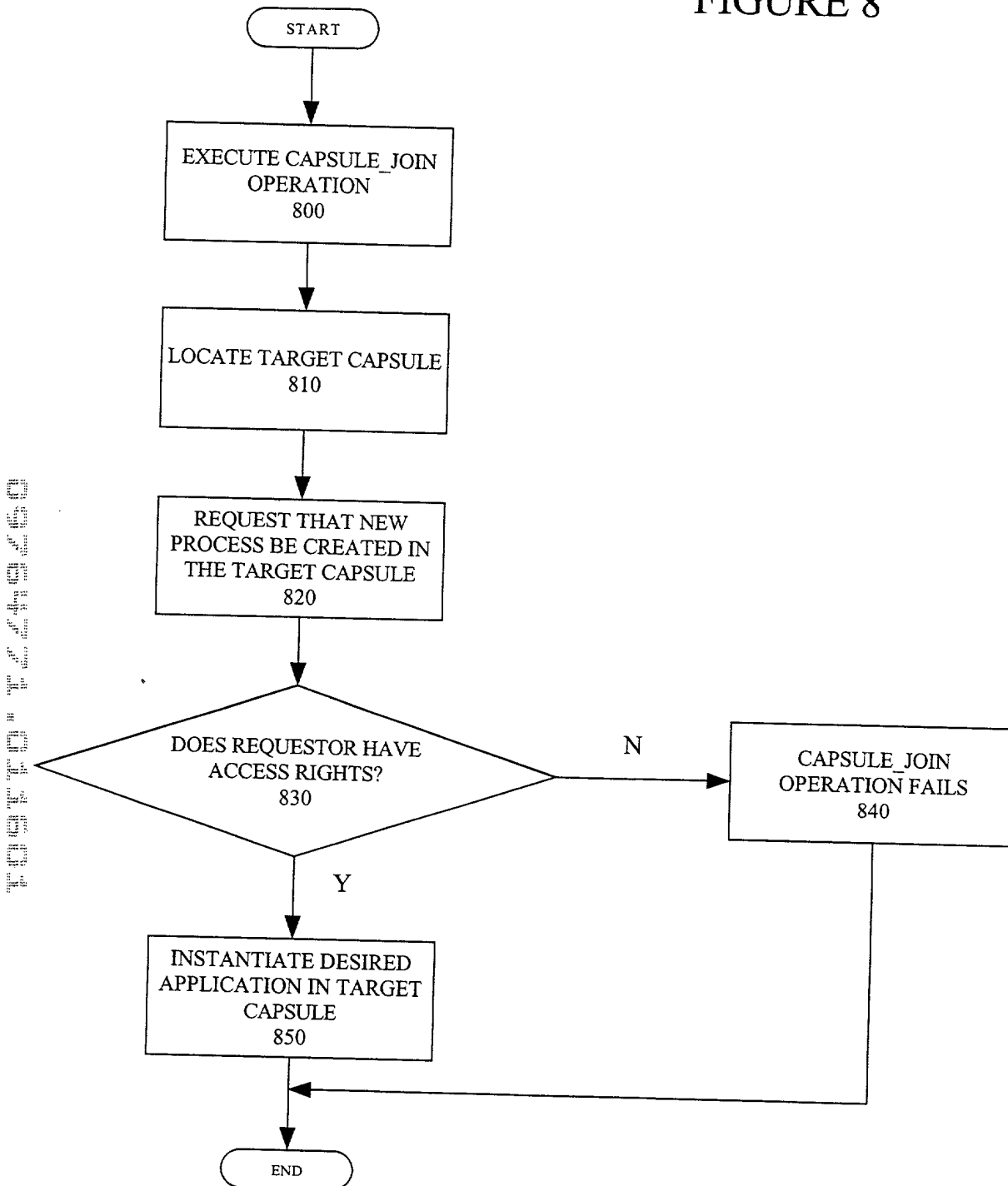
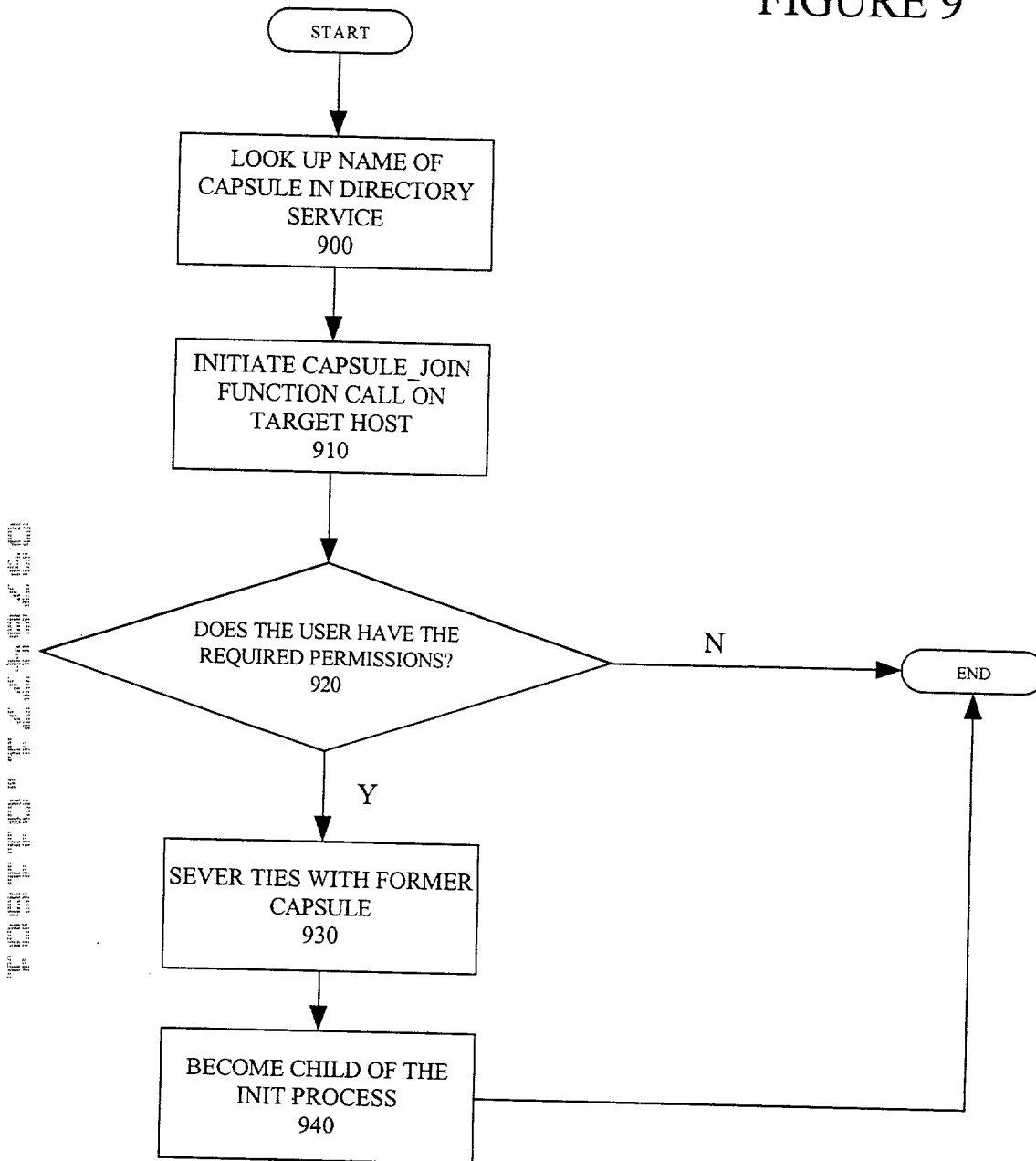


FIGURE 9



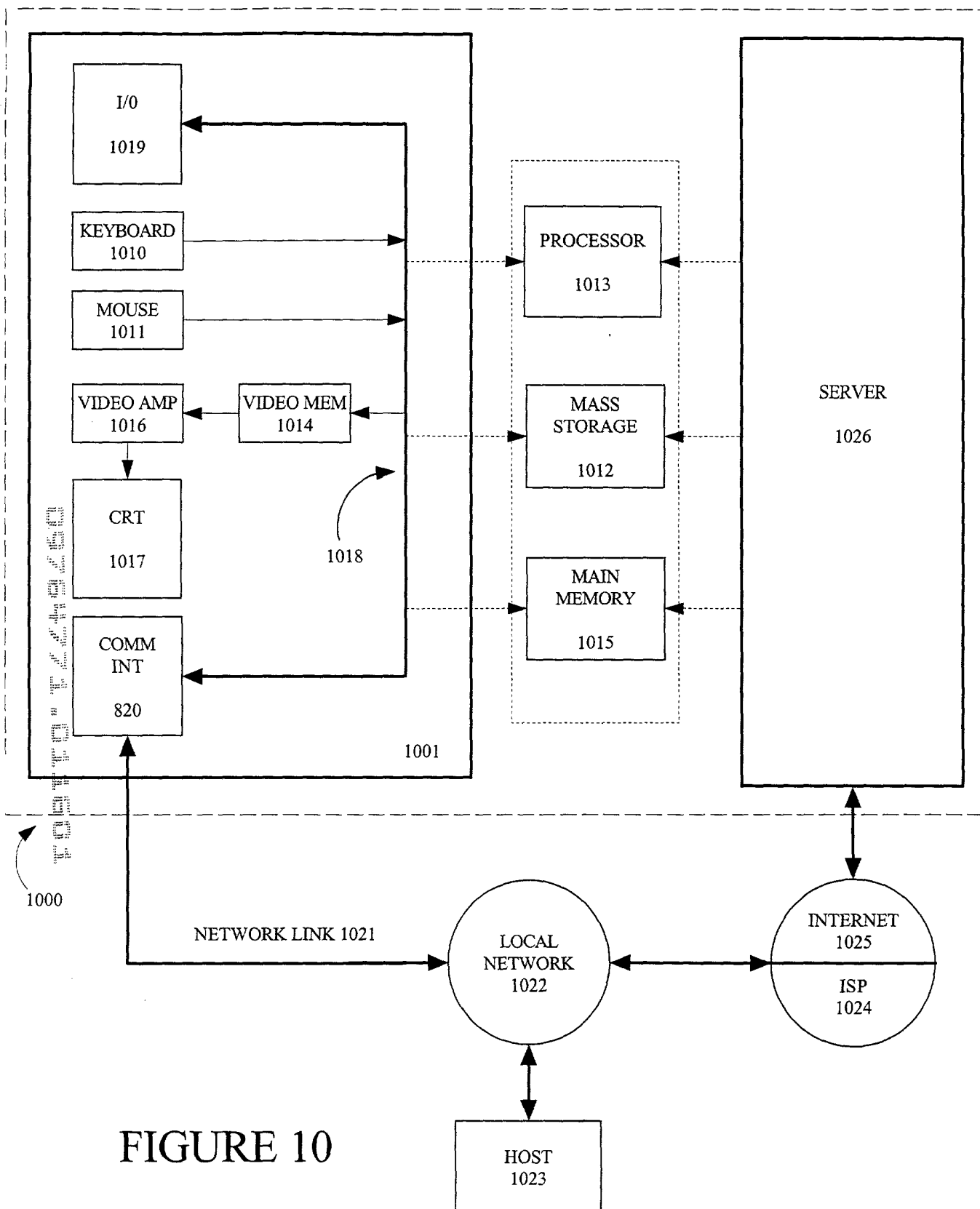


FIGURE 10

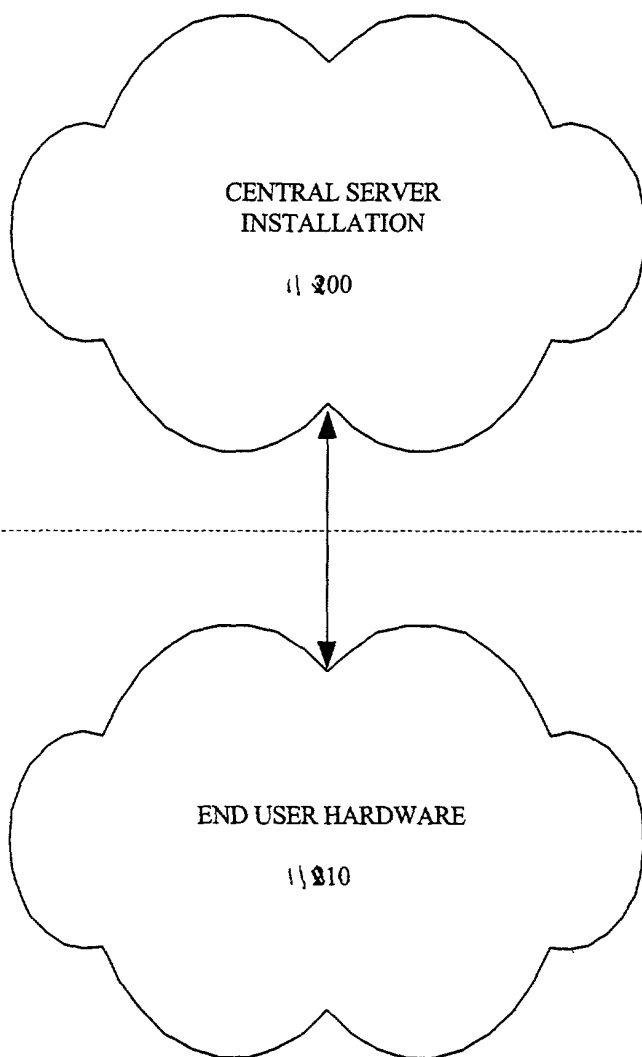


FIGURE 11

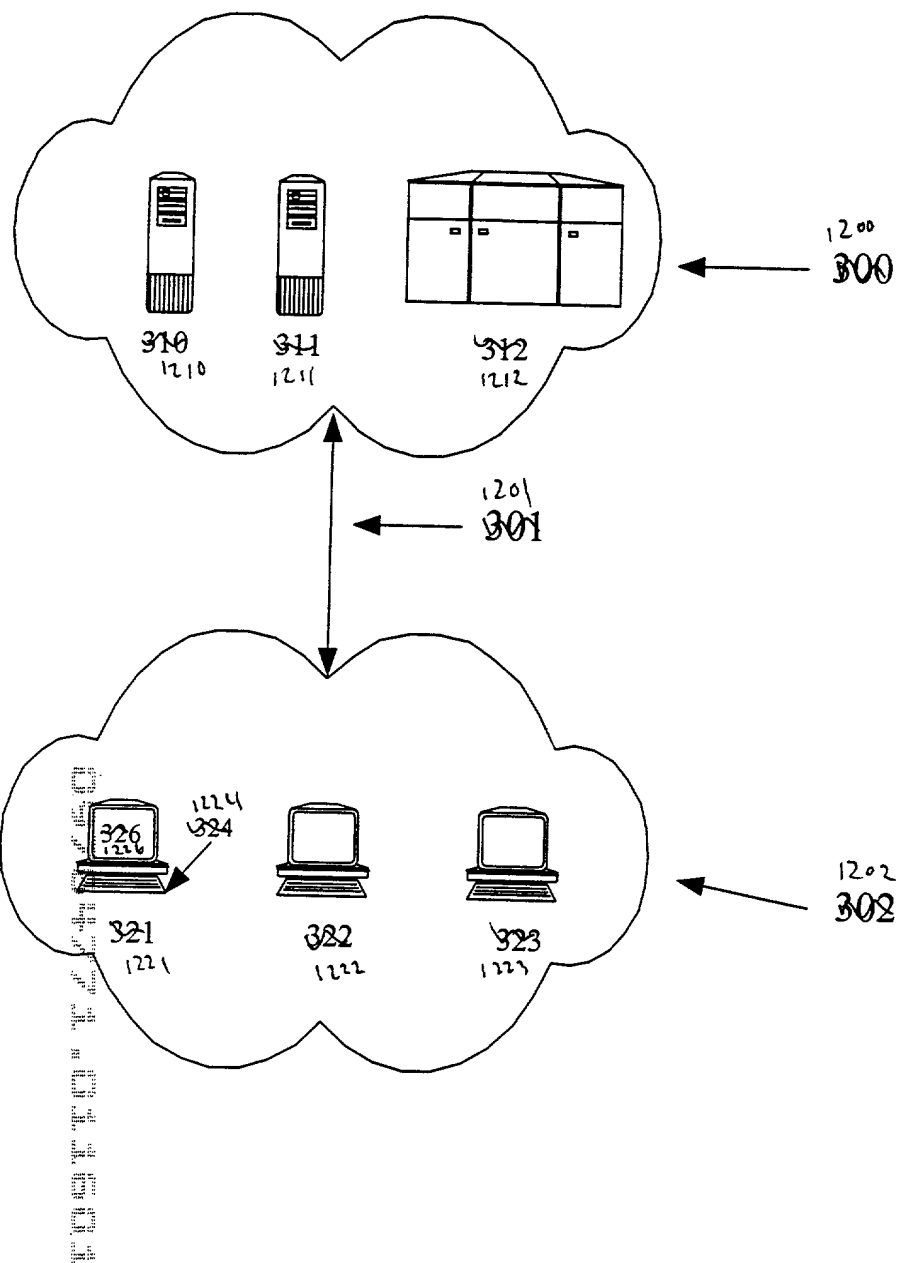
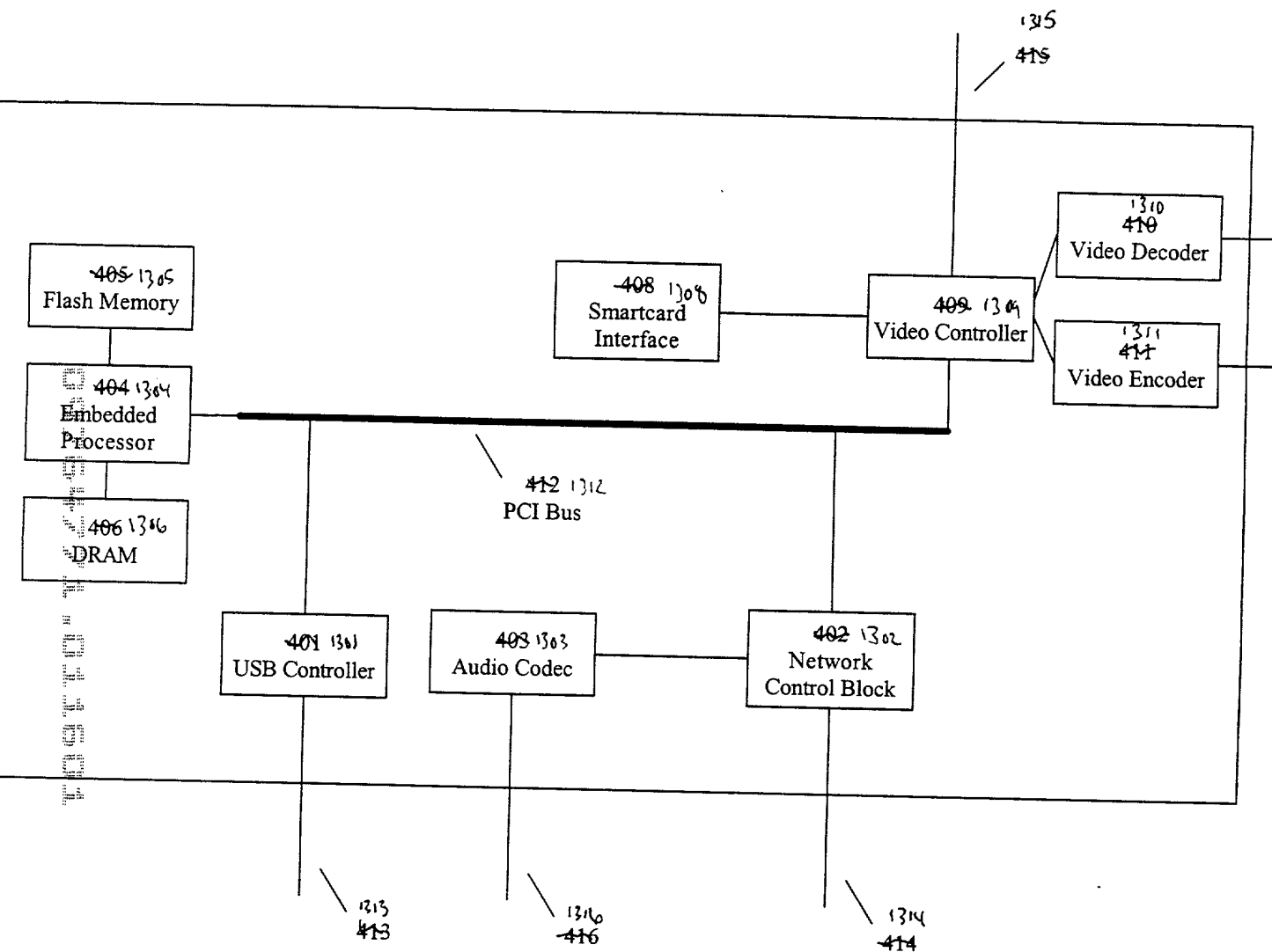
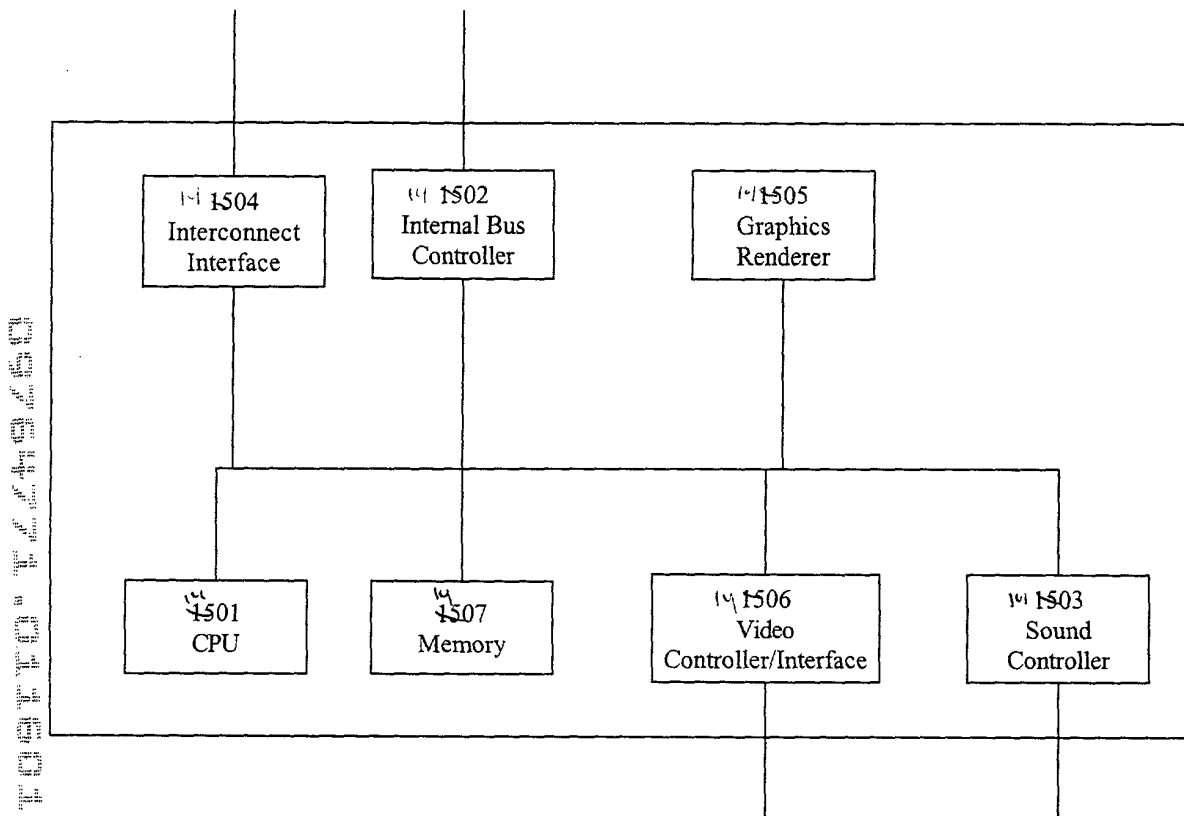


FIGURE 3 12



13
Figure 4: HID



14
Figure 15: HID Single Chip Implementation

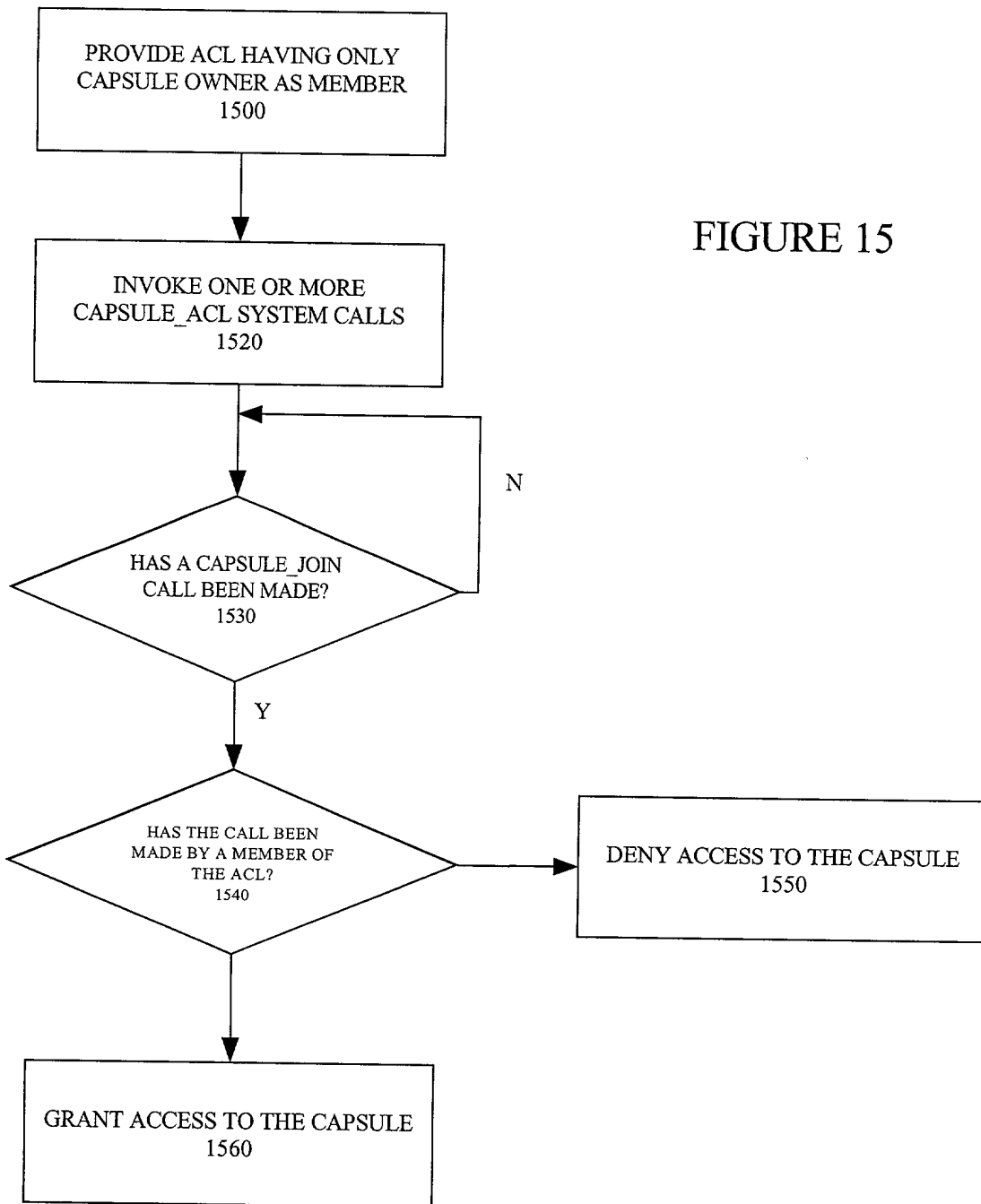


FIGURE 15